

# DRIVING METHOD FOR PLASMA DISPLAY PANEL AND PLASMA DISPLAY DEVICE

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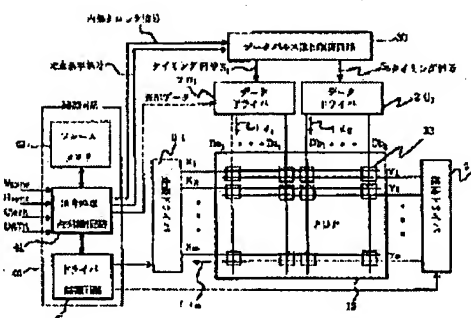
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## Abstract of JP2001272948

**PROBLEM TO BE SOLVED:** To provide a plasma display device capable of reducing noise and also capable of reducing power consumption by lowering peak values of respective electrode currents. **SOLUTION:** Data electrodes of this plasma display panel 15 are divided into plural data electrode groups. A data-pulse phase control circuit 30 outputs pulse trains which consist of plural pulse trains and in which the starting timing of first pulses are delayed and the completing timing of final pulses are made to be early with different phases for every data electrode group based on an internal clock signal and a scanning reference signal from a control circuit 40. Data drivers 201, 202 provided for every data electrode group output display data in synchronization with pulse trains outputted by the data-pulse phase control circuit 30.



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